Electronics World

www.electronicsworld.co.uk

IN THIS ISSUE:

- lloT
- Medical
- Rail
- EMC

March 2023 Volume 128 Issue 2025 £5.90



Ethernet at the edge o industrial networks



Contents

Regulars

04 Trend

- 05 Technology
- **08** Circuit Drill
 - By Sulaiman Algharbi Alsayed, Managing Director, Smart PCB Solutions
- 10 EMC

Dr Min Zhang, EMC consultant, Mach One Design

12 Optical Isolation

By Professor Murat Uzam, Department o Electrical and Electronics Engineering, Yozgat Bozok University, Turkey

- **14 Electric Vehicles** By Vicor design engineers
- 41 Products
- 42 Contact directory

43 Web locators



Cover supplied by Microchip Technology. More on pages 6-7

Features

18 How does cold weather affect electric vehicles?

By Vivienne Kerry, Business Manager – EV Connectors, Foremost Electronics

20 The design challenges for electronic systems on board trainss By Jasmin Sommerfeldt. Mechanical and

Thermal Design Engineer, Duagon

- 22 Designing with DC-DC converters By Ann-Marie Bayliss, Senior Product Marketing Manager, and John Quinlan, Strategic Technical Marketing Manager, Murata
- 26 Capacitors for railways require specialist technologies and designs By Jens Heitmann, Account and Marketing
- 28 Reaching for satellites to ensure seamless, global IoT interoperability

- 30 Al-enabled sensor fusion improves industrial processes By Suad Jusuf, Senior Manager,
- **32 Designing modern medical equipment requires secure solutions** By Silicon Motion design and development engineers
- **34 Medical device cybersecurity** By Joe Lomako, Business Development Manager (IoT), TÜV SÜD
- 36 Making environmentally-responsible electronics By Emma Armstrong, Group Commercial and Electronics Sustainability Director, In2tec
- 38 Source of two harmonic voltages that can be mutually phase shifted bipolarly
 By Marián Štofka, Slovak University of Technology, Bratislava, Slovakia



EDITOR: Stella Josifovska Tel: +44 (0)1732 883392 Email: svetlanaj@electronicsworld.co.uk

SALES: Louise Tiller Tel: +44 (0)1622 699104 Email: Itiller@datateam.co.uk

Harriet Campbell Tel: +44 (0)1622 699184 Email: hcampbell@datateam.co.uk

PRODUCTION/DESIGN: Tania King Email: tking@datateam.co.uk

MEDIA DIRECTOR: Louise Tiller Tel: +44 (0)1622 699104 Email: Itiller@datateam.co.uk

ISSN: 1365-4675 PRINTER: Buxton Press Ltd

SUBSCRIPTIONS: Subscription rates: 1 year digital only - £40 1 year print & digital (UK only) - £75 1 year print & digital (Overseas) - £180

Email: membership@electronicsworld.co.uk www.electronicsworld.co.uk/subscribe



Follow us on Twitter @electrowo Join us on

LinkedIn







t: +44(0)1460 230000 e; sales@euroquartz.co.uk w: www.euroquartz.co.uk

Optically-isolated analogue output module for a 0-5V to 4-20mA signal converter

By Professor Murat Uzam, Department of Electrical and Electronics Engineering, Yozgat Bozok University, Turkey

n this series of articles about signal coverters, this month we will present the seventh – and last in the series – output module, this time for a 0-5V to 4-20mA converter that provides current from 4-20mA and requires two DC power supplies: +12V and isolated +12V.

Figure 1 shows the module, which connects to the DAC output of a 5V microcontroller; see Figure 2. This circuit contains the Positive Unipolar Photovoltaic Isolation Amplifier 3 (PUPIA3), which we covered before, with an HCNR201 high-linearity analogue optocoupler providing photovoltaic isolation. The circuit's input (to the left of HCNR201) is isolated from its output. Due to the limited current drive capability, the buffer amplifier (a voltage follower) LM358P-1A is used on the DAC output, whose output is connected to the input of PUPIA3. Here, PUPIA3 consists of: 1. Input: R1, R2, LM358P-1B;

- 2. HCNR201;
- 3. Output: P1, R3, C3, LM358P-2A. Provided that the input voltage of the PUPIA3 is limited by $0.00V \le V_{IN} \le 5.00V$,

its output voltage will also be limited between 0.00V and 5.00V. The output of PUPIA3 is connected to the non-inverting input terminal of the buffer amplifier LM358P-2B.

Design based on XTR116

This design is based on the XTR116 from Texas Instruments, a precision current output converter that feeds 4-20mA signals over an industry-standard current loop. It provides accurate current scaling and output current limit functions.

XTR116 operates over an extended industrial temperature range (-40°C to +85°C), offers low quiescent current (200 μ A), 5V regulator for external circuits, V_{REF} for sensor excitation (4.096V), low span error (0.05%), low nonlinearity error (0.003%) and a wide loop supply range (7.5-36V). Applications include 2-wire, 4-20mA current loop transmitters, industrial process control, test systems compatible with Hart modems, current amplifiers, voltage-tocurrent amplifiers, and more.

The XTR116 is found in most PLCs at the heart of modern automation products. It is the least expensive 4-20mA current loop output IC and it is very accurate.

The XTR116 is a two-wire current transmitter, where its input signal (pin 2) controls the output current. A portion of this current flows into the V+ power supply, pin 7. The remaining current flows in the external transistor, BD139.

The XTR116 is a current-input device with a gain of 100; i.e., $I_{OUT} = 100 \cdot I_{IN}$. The input voltage at the I_{IN} pin is zero (referred to the I_{RET} pin). A voltage input is created with an external input resistor (P1 + R1).

Common full-scale input voltages range from upward of 1V; fullscale inputs greater than 0.5V are recommended to minimise the effects of offset voltage and drift of the internal operational wamplifier A1.

The XTR116 is designed to use an external transistor to avoid on-chip thermalinduced errors. Transistor BD139 conducts the majority of the full-scale output current. Power dissipation in this transistor can approach 0.8W with high loop voltage (40V) and 20mA output current.

The XTR116 provides accurate, linear output up to 25mA. Internal circuitry limits the output current to approximately 32mA, to protect the transmitter and loop power/measurement circuitry. The XTR116 low compliance voltage rating (7.5V) allows the use of various voltage protection methods without compromising the operating range. A diode bridge circuit, obtained by using 1N4148 diodes, allows normal operation, even when the voltage connection lines are reversed. The bridge causes a two diode drop loss (approximately 1.4V) in loop supply voltage, which results in a compliance voltage of about 9V - suitable for most applications.



Figure 1: The output module of the optically-isolated 0-5V to 4-20mA signal converter

5.00	20
4.75	19
4 50	18
4.50	10
1 25	17
4,25	17
4.00	16
4.00	10
3.75	15
••	
3.50	14
••*:	
3.25	13
3.00	12
-a	á.
2.75	11
2.50	10
2.25	9
•+	4.
2.00	8
·*:	
1.50	6
1.25	5
12	
1.00	4
0.75	3
0.50	2
0.00	-
0.25	
0.25	1
••:	•••

Table 1: Modules 1 and 2 example voltages, assuming $0.00V \le V_{iN} \le 5.00V$



*: It is assumed that $0.00 \text{V} \le \text{Vin} \le 5.00 \text{V}$. When $0.00 \text{V} \le \text{Vin} \le 5.00 \text{V}$, Iout =(Vin / 0.25) mA.

Figure 2: The module's connections



Figure 3: Iour vs VIN for the module

Remote connections to current transmitters can sometimes be subjected to voltage surges. Therefore, it is prudent to limit the maximum surge voltage applied to the XTR116 as low as practically possible. A 27V TVS (Transient Voltage Suppressor) is used to filter and suppress any transients coming from the current input and output terminals.



Figure 4: The module's prototype circuit board

A 27V TVS is chosen for a 24V external power supply V_{PS} . For $V_{PS} \ge 24V$, this TVS must be replaced with an appropriate one. For example, when $V_{PS} = 36V$, a 39V TVS must be used.

Setup and calibration

Here we assumed that $V_{_{\rm IN}}$ is taken from the DAC output of a 5V microcontroller with 0.00V \leq $V_{IN} \le 5.00V$. When $0.00V \le V_{IN} \le 5.00V$, $I_{OUT} =$ $(V_{IN}/0.25)$ mA. Input voltage range $V_{IN} = 0.5.00$ V and therefore output current range $I_{OUT} = 0-20$ mA; see Figure 3.

Table 1 provides some example input voltages and output currents for this signal converter module, with its prototype circuit board shown in Figure 4.

- To calibrate it, do the following:
- 1. $V_{ps} = 24V$ and $R_t = 250\Omega$.
- 2. Set V_{IN} to +5.00V and, then, by adjusting P1 make $V_{TP1} = +5.00$ V.
- 3. By adjusting P2 make sure that when $\mathrm{V_{_{IN}}}$ = 0.00V, $\mathrm{V_{_{RL}}}$ = 0.00V and, also, when $V_{IN}^{IN} = +5.00V, V_{RL}^{IL} = +5.00V.$

